



PATENT
8017-1130

IN THE U.S. PATENT AND TRADEMARK OFFICE

re application of

Shinpei IIJIMA et al.

Conf. 1592

Application No. 10/801,003

Group 2823

Filed March 16, 2004

Examiner Joannie A. Garcia

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND
METHOD OF MANUFACTURING THE DEVICE

PETITION FOR EXTENSION OF TIME

Assistant Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

October 3, 2005

Sir:

The undersigned hereby petitions for an extension of time to respond to the Official Action of June 3, 2005 for one month to October 3, 2005.

Please charge the extension fee of \$120.00 to Deposit Account No. 25-0120. If this fee is insufficient, the Patent Office is hereby authorized to charge any additional extension fee to Deposit Account No. 25-0120. A duplicate copy of this sheet is enclosed.

A responsive paper is filed herewith.

Respectfully submitted,

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